

***Remarks***

Reconsideration of this Application is respectfully requested.

Upon entry of the foregoing amendment, claims 8, 10, 16, 17, 21, and 22 are pending in the application, with claims 8 and 18 being the independent claims. Claims 8, 10, 16-18, 21, and 22 are sought to be amended. Claims 10, 11, 18, 19, and 20 are sought to be cancelled. Applicants reserve the right to prosecute similar or broader claims, with respect to the cancelled and/or amended claims, in the future. These changes are believed to introduce no new matter, and their entry is respectfully requested.

With respect to this Application, Applicants hereby rescind any disclaimer of claim scope made in the parent application or any predecessor or related application. The Examiner is advised that any previous disclaimer of claim scope, if any, and the alleged references that it was made to allegedly avoid, may need to be revisited. Nor should any previous disclaimer of claim scope, if any, in this Application be read back into any predecessor or related application.

Based on the above amendment and the following remarks, Applicants respectfully request that the Examiner reconsider all outstanding rejections and that they be withdrawn.

***Rejections under 35 U.S.C. § 102***

**Claims 8, 10, 11, 13, 16, and 17**

Claims 8, 10, 11, 13, 16, and 17 stand rejected under 35 U.S.C. § 102(e) as allegedly being anticipated by United States Patent Publication No. 2004/0225977 to

Akkerman (herein "Akkerman"). Applicants respectfully traverse the rejection and provide the following arguments to support patentability.

Applicants respectfully submit that claim 8 as amended is patentable over Akkerman. For example, Akkerman does not teach or suggest at least the features of "*a second asynchronous clock domain, coupled to the first asynchronous clock domain, including a first jitter element and a second jitter element, wherein the first jitter element includes a delay element and a multiplexer, the first jitter element being configured to introduce predetermined timing delays which are randomly exercised into data from the first asynchronous clock domain, the first jitter element being insertable in the second asynchronous clock domain at a circuit boundary between the first asynchronous clock domain and the second asynchronous clock domain, and the second jitter element includes an x generator element configured to introduce predetermined signal values which are randomly generated into the data from the first asynchronous clock domain*" as recited by amended claim 8. Dependent claims 13 and 16 are likewise not anticipated by Akkerman for the same reasons as claim 1 from which they depend and further in view of their own respective features. Without acquiescing to the merits of this allegation, Applicants have cancelled claims 10, 11, and 17 rendering the rejection of claims 10, 11, and 17 under 35 U.S.C. § 102(e) moot. Accordingly, Applicants respectfully request that the rejection of claims 1-6 and 12 under 35 U.S.C. § 102(e) be reconsidered and withdrawn.

***Rejections under 35 U.S.C. § 103***

**Claims 12 and 18-22**

Claims 12 and 18-22 stand rejected under 35 U.S.C. § 103(a) as allegedly being unpatentable over Akkerman (herein "Akkerman"). Applicants respectfully traverse the rejection and provide the following arguments to support patentability.

As discussed above, Akkerman does not teach or suggest each and every feature of claim 8. Dependent claim 12 is likewise not rendered obvious by Akkerman for the same reasons as claim 8 from which it respectively depends and further in view of its own respective features. Accordingly, Applicants respectfully request that the rejection of claim 12 under 35 U.S.C. § 103(a) be reconsidered and withdrawn.

Applicants respectfully submit that claim 8 as amended is patentable over Akkerman. For example, Akkerman does not teach or suggest at least the features of "*a second asynchronous clock domain, coupled to the first asynchronous clock domain, including a first jitter element and a second jitter element, wherein the first jitter element includes a delay element and a multiplexer, the first jitter element being configured to introduce predetermined timing delays which are randomly exercised into data from the first asynchronous clock domain, the first jitter element being insertable in the second asynchronous clock domain at a circuit boundary between the first asynchronous clock domain and the second asynchronous clock domain, and the second jitter element includes an x generator element configured to introduce predetermined signal values which are randomly generated into the data from the first asynchronous clock domain*

" as recited by amended claim 18. Dependent claims 21 and 22 are likewise not anticipated by Akkerman for the same reasons as claim 1 from which they depend and further in view of their own respective features. Without acquiescing to the merits of

this allegation, Applicants have cancelled claims 19 and 20 rendering the rejection of claims 10, 11, and 17 under 35 U.S.C. § 102(e) moot. Accordingly, Applicants respectfully request that the rejection of claims 18, 21, and 22 under 35 U.S.C. § 102(e) be reconsidered and withdrawn.

### ***Conclusion***

All of the stated grounds of objection and rejection have been properly traversed, accommodated, or rendered moot. Applicants therefore respectfully request that the Examiner reconsider all presently outstanding objections and rejections and that they be withdrawn. Applicants believe that a full and complete reply has been made to the outstanding Office Action and, as such, the present application is in condition for allowance. If the Examiner believes, for any reason, that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at the number provided.

Prompt and favorable consideration of this Amendment and Reply is respectfully requested.

Respectfully submitted,

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Date: 18 May 2009

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